



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE

United States Patent and Trademark Office

Address: COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/826,488	04/16/2004	Eric Jeffrey	VP115	1354
20178 7590 07/07/2009 EPSON RESEARCH AND DEVELOPMENT INC INTELLECTUAL PROPERTY DEPT 2580 ORCHARD PARKWAY, SUITE 225 SAN JOSE, CA 95131				
EXAMINER				
ALMEIDA, CORY A				
ART UNIT		PAPER NUMBER		
2629				
MAIL DATE		DELIVERY MODE		
07/07/2009		PAPER		

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/826,488

Applicant(s)

JEFFREY ET AL.

Examiner

CORY A. ALMEIDA

Art Unit

2629

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 20 April 2009.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-3,5-7 and 15-20 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-3,5-7 and 15-20 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-8508)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date _____
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____

DETAILED ACTION

Claims 4 and 8-14 are cancelled.

Claims 1-3, 5-7, and 15-20 are pending

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-3, 7, and 15-18, are rejected under 35 U.S.C. 102(b) as being anticipated by Willis 5329369.

3. In regards to claim 1 and the associated claim 15, Willis discloses a method for displaying an image in an electronic medium (Abstract), comprising utilizing a display controller to receive an image from a camera for display (Col. 2, 5-10), storing a first set of dimension values in one or more registers in the display controller, the first set of dimension values specifying a display region in which a resized camera image is displayed (Col. 12, 11-32), storing a second set of dimension values in one or more registers in the display controller, the second set of dimension values specifying a picture-in-picture (PIP) window (Col. 12, 33-53), changing the first set of dimension values, wherein the resized camera image being displayed remains undisturbed when changing the first set of dimension values (Col. 12, 11-32), changing the second set of dimension values, wherein the picture-in-picture (PIP) window remains undisturbed when changing the second set of dimension values (Col. 12, 33-53), providing a

completion signal indicating completion of changing the first set of dimension values and the second set of dimension values (Col. 12, 21-53 discloses changing sizes of both video signals which means that after they are changed they would be completed), receiving a trigger signal indicating a beginning of a new image to be displayed (Col. 6, 50 – Col. 7, 6), and implementing the changed first set of dimension values and the changed second set of dimension values upon receiving the trigger signal while the completion signal is being provided (Col. 21, 30-35).

4. In regards to claim 2, Willis discloses changing the first set of dimension values and the second set of dimension values spans a number of trigger signal receptions while the completion signal has not yet been provided (Col. 12, 21-53 discloses changing sizes of both video signals which includes a finite amount of time before it is completed).

5. In regards to claim 3, Willis discloses operating image synchronization logic to recognize receipt of the trigger signal while the completion signal is being provided, and operating the image synchronization logic to cause an essentially simultaneous implementation of the changed first set of dimension values and the changed second set of dimension values (Col. 21, 30-35).

6. In regards to claim 7, Willis discloses the image is a camera image (Col. 2, 5-29) and the trigger signal is a VSYNC signal indicating a new frame of the camera image (Col. 11, 38-51).

7. In regards to claim 16, Willis discloses the image synchronization circuitry is configured to recognize a vertical synchronization signal as the trigger signal, the

vertical synchronization signal to be provided in conjunction with image data to be received by the display controller (Col. 11, 38-51).

8. In regards to claim 17, Willis discloses the image synchronization circuitry is configured to implement the dimension values stored in each of the first set of dimension registers and the second set of dimension registers in a simultaneous manner (Col. 2, 42-64).

9. In regards to claim 18, Willis discloses camera interface circuitry configured to receive data defining the image to be displayed (Col. 11, 32-51), and resizer circuitry configured to adjust a size of the image to be displayed in accordance with dimension values stored in the first set of dimension registers, the resizer circuitry further configured to be controlled by the image synchronization circuitry (Col. 12, 21-53).

Claim Rejections - 35 USC § 103

10. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

11. Claims 5-6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Willis 5,329,369.

12. In regards to claim 5, Willis discloses a video ram or memory (Abstract, video memory and Col. 13 12-35).

Willis does not disclose expressly providing the completion signal is performed by setting an enable bit within a last register changed, wherein the last register changed represents a final register required to have its dimension value changed.

However, at the time of the invention, it would have been obvious to one of ordinary skill in the art that a write enable or completion bit could be stored in the memory Willis discloses.

The motivation for doing so would have been for error prevention.

Therefore, it would have been obvious that Willis discloses the invention of claim 5.

13. In regards to claim 6, Willis discloses a video ram or memory (Abstract, video memory and Col. 13 12-35).

Willis does not disclose expressly changing the first set of dimension values stored in the one or more registers specifying a display region in which a resized camera image is displayed is performed by duplicating changes made to the second set of dimension values stored in the one or more registers specifying the PIP window.

However, at the time of the invention, it would have been obvious to one of ordinary skill in the art that one could duplicate dimension values from one memory location to another to make the main window and PIP window the same size.

The motivation for doing so would have been for showing two images with equal focus as shown in Willis Fig. 1d.

Therefore, it would have been obvious that Willis discloses the invention of claim 6.

14. Claims 19, and 20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Willis 5,329,369 in view of Duhault, US-6456334.

15. In regards to claim 19, Willis does not disclose expressly the display controller is incorporated into a portable electronic computing device.

Duhault discloses a multiple window or PIP laptop (Fig. 1, Col. 1 41-45 and Col. 2 16-31).

At the time of the invention it would have been obvious that the PIP device of Willis could be incorporated into a device such as a laptop as Duhault discloses.

The motivation would have been to present multiple images to a user simultaneously in a smaller form factor such as a laptop.

Therefore it would have been obvious to combine Duhault with Willis to obtain the invention as specified in claim 19.

16. In regards to claim 20, Duhault discloses the portable electronic computing device is selected from the group consisting of a cellular phone, a personal digital assistant, a web tablet, and a pocket personal computer (Col. 2, 16-31).

Response to Arguments

17. Applicant's arguments filed 4/20/2009 have been fully considered but they are not persuasive.

18. Applicant argues that present application and Willis are directed towards two different problems. However this is not persuasive as references cited do not have to pertain to the same problem or even the same technology as long as the claim language can be read on the reference.

Conclusion

19. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to CORY A. ALMEIDA whose telephone number is (571) 270-3143. The examiner can normally be reached on Monday through Friday 8AM to 4PM EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Alexander Eisen can be reached on 571-272-7687. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/KEVIN M NGUYEN/
Primary Examiner, Art Unit 2629

CA

7/2/2009